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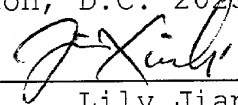
Docket No. TY-001

Applicants : Gosuke Oshima et al.  
For : ELECTRONIC DEVICE MANUFACTURING METHOD,  
ELECTRONIC DEVICE AND RESIN FILLING METHOD

EXPRESS MAIL CERTIFICATION

"Express Mail" mailing label number EL624700817US.  
Date of Deposit July 12, 2000

I hereby certify that this transmittal letter and the other papers and fees identified in this transmittal letter as being transmitted herewith are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and are addressed to the Hon. Assistant Commissioner for Patents, Washington, D.C. 20231.



Lily Jiang

Hon. Assistant Commissioner  
for Patents  
BOX PATENT APPLICATION  
Washington, D.C. 20231

TRANSMITTAL LETTER FOR  
ORIGINAL PATENT APPLICATION

Sir:

Transmitted herewith for filing are the  
[X] specification; [X] claims; [X] abstract; [X] declaration  
and [X] power of attorney, for the above-identified patent  
application.

Also transmitted herewith are:

[X] 12 sheets of:

- [ ] Formal drawings.
- [X] Informal drawings. Formal drawings will be filed during the pendency of this application.

[X] Certified copy(ies) of application(s)

Japan	11-198987	July 13, 1999
(country)	(appln. no.)	(filed)

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from which priority is claimed.

[ ] An assignment of the invention to \_\_\_\_\_.

[ ] A check in the amount of \$40.00 to cover the recording fee.

[ ] Please charge \$40.00 to Deposit Account No. 06-1075 in payment of the recording fee. A duplicate copy of this transmittal letter is transmitted herewith.

[ ] An associate power of attorney.

[X] PrintEFS Data Sheet.

The filing fee has been calculated as shown below:

FOR	NUMBER FILED	NUMBER EXTRA	RATE	FEE
BASIC FEE				\$690.00
TOTAL CLAIMS	51	- 20 =	31 X \$ 18 =	\$558.00
INDEPENDENT CLAIMS	3	- 3 =	3 X \$ 78 =	\$0.00
[ ] MULTIPLE DEPENDENT CLAIMS			+ \$260 =	\$260.00
			TOTAL	<u>\$1508.00</u>

[X] A check in the amount of \$ 1508.00 in payment of the filing fee is transmitted herewith.

[ ] This application is being filed unaccompanied by a filing fee. The appropriate filing fee will be paid in response to a Notice to File Missing Parts, pursuant to 37 C.F.R. § 1.53(f).

[X] The Commissioner is hereby authorized to charge payment of any additional filing fees required under 37 C.F.R. § 1.16, in connection with the paper(s) transmitted herewith, or credit any overpayment of same, to deposit Account No. 06-1075. A duplicate copy of this transmittal letter is transmitted herewith.

[ ] Amend the specification by inserting before the first line the sentence: -- This is a [ ] continuation-in-part, of application Serial No.: \_\_\_\_\_, filed  
entitled \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_.

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Nicola A. Pisano  
Registration No. 34,408

Norman H. Beamer  
Registration No. 32,721  
Attorneys for Applicants

FISH & NEAVE  
Customer No. 1473  
1251 Avenue of the Americas  
New York, New York 10020-1104  
Tel.: (650) 617-4000

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CORRESPONDENCE INFORMATION

APPLICATION INFORMATION

## REPRESENTATIVE INFORMATION

Source:: PrintEFS Version 1.0.1

TY-001

ELECTRONIC DEVICE MANUFACTURING METHOD, ELECTRONIC  
DEVICE AND RESIN FILLING METHOD

Field Of The Invention

5 The present invention relates to (a) an  
electronic device manufacturing method and an  
electronic device which eliminates waste of materials  
and unnecessary manufacturing processes, and (b) a  
resin filling method in which a target region  
enveloping electronic parts is filled with a resin.

10 Background Of The Invention

In the past, there has been a rapid spread of  
compact electronic devices in which a plurality of  
electronic parts are mounted on a substrate.  
Electronic devices of this type include devices which  
15 are sealed or molded by means of a resin as in the case  
of ICs and hybrid modules, or the like. Electronic  
devices of this type also includes devices which are  
accommodated in a metal case or in which the electronic  
parts are covered by a metal case.

20 In the manufacture of such electronic devices,  
substrates have been prepared for individual electronic  
devices, and electronic parts have been mounted on  
these substrates, after which sealing or molding using

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a resin, or the mounting of a metal case, had been performed.

Problems To Be Solved By The Invention

The following problems are encountered in  
5 manufacturing the above-mentioned electronic devices.

First of all, in the case of devices that are resin-sealed or molded by means of a resin, since there are indentations and projections on the surfaces of the electronic devices, the electronic devices may fall  
10 from the suction chucking machine and suction chucking is difficult to accomplish when the devices are mounted on motherboards using a suction chucking type automatic mounting machine.

Secondly, in the case of electronic devices  
15 that use a metal case, although such devices are superior in that they can be shielded from electromagnetic fields and the electronic parts on the substrate can be protected, a process in which metal cases are mounted on the individual substrates is  
20 required, and this is an obstacle to the reduction of manufacturing costs.

Thirdly, since compact substrates are formed for individual electronic devices, and the work of mounting electronic parts on these substrates is  
25 automated using machinery, extra parts must be provided in order to fasten the individual substrates in place during shipping and part mounting operations, so that a waste of substrate material is generated. Such extra parts used to fasten individual substrates in place are  
30 generally called "fastening parts" or "ears" by persons familiar with the art. The electronic devices are ultimately shipped as finished products after these

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"fastening parts" or "ears" have been cut off and discarded.

Fourth, it is extremely difficult to accomplish resin sealing of a single electronic part mounted on a substrate in isolation from the area surrounding said electronic part. In the past, therefore, considering ease of manufacturing, the entire electronic part has been resin-sealed, or resin sealing has been performed with the electronic parts surrounding the target electronic part included, even in cases where only a single electronic part requires resin sealing. This has resulted in a waste of sealing resin.

In light of the above problems, the object of the present invention is to provide an electronic device manufacturing method and an electronic device which make it possible to (1) reduce the waste of materials, (2) reduce the number of manufacturing steps, and (3) limit resin sealing to a specified target region.

#### Summary Of The Invention

A. In order to achieve the above-mentioned objects, the present invention proposes a method for manufacturing an electronic device which has a substrate, electronic parts that are mounted on the main surface of said substrate, and a resin part that is formed on the main surface of the aforementioned substrate so that said resin part fills a specified space surrounding at least one electronic part, said electronic device manufacturing method comprising a step in which a collective substrate consisting of a plurality of substrates linked in the form of a matrix is formed, a step in which electronic parts are mounted

on the main surface of the aforementioned collective substrate, a step in which the aforementioned resin part which is solidified so that it covers the aforementioned electronic parts is formed on the main surface of the collective substrate on which the  
5 aforementioned electronic parts have been mounted, and a step in which the collective substrate on which the aforementioned resin part has been formed is separated into individual substrates.

10 B. Additional benefits can be obtained by adding to the method described in the previous paragraph a step in which terminal electrode members are conductively connected and mounted across specified adjacent substrates on the upper surface of the  
15 collective substrate prior to the formation of the aforementioned resin part, so that these terminal electrode members cross the boundary lines of the adjacent substrates. The aforementioned resin part is formed to a specified thickness over the entire main  
20 surface of the collective substrate during the step in which the resin part is formed, and the terminal electrode members are cut in the step in which the collective substrate on which the aforementioned resin part has been formed is separated into individual  
25 substrates.

C. The aforementioned terminal electrode members are cut in the step in which the aforementioned collective substrate is separated into individual substrates; accordingly, the cut surfaces of the  
30 terminal electrode members are exposed at the cut surfaces of the aforementioned resin part, so that the terminal electrode members can be used as terminal electrodes. As a result, working of the terminal electrodes can easily be accomplished, and electronic

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devices manufactured by this method can be mounted with the side surfaces of said devices, i.e., the aforementioned cut surfaces, facing the motherboard. In addition, an intermediate layer consisting of an insulating elastic material can be formed on the main surface of the collective substrate (on which the  
5 aforementioned electronic parts have been mounted) prior to the formation of the resin part, so that said intermediate layer covers the electronic parts. This  
10 intermediate layer alleviates stresses generated by thermal expansion. Differences in the thermal expansion coefficients of the substrate, solder and resin part are alleviated by the intermediate layer.

D. Furthermore, in the methods described above,  
15 it is possible to use a vacuum printing method to form the resin part, so that a resin part having a specified thickness is formed over the entire main surface of the collective substrate. The use of a vacuum printing method allows formation of a resin part without any  
20 gaps around the electronic parts. Vacuum printing can be accomplished, for example, using Vacuum Printing Encapsulation Systems such as are available from Japan Rec. Co. LTD. of Takatsuki-shi, Osaka, Japan.

E. Furthermore, it is possible to dispose a  
25 resin in a vacuum state in a region which includes at least one of the aforementioned electronic parts on the main surface of the aforementioned substrate and a specified space surrounding said electronic part, in isolation from other regions, and a step in which a  
30 further resin layer is subsequently formed on the surface of the aforementioned resin part in either a non-vacuum state or vacuum state. The further resin layer is formed on the surface of the resin part which, having been formed using a vacuum printing method, is



Figure 1 consists of 15 bar charts, labeled (a) through (o), each representing a different fish species. The y-axis for all charts is 'Percentage of total catch' ranging from 0 to 100. The x-axis for each chart represents a specific year or period. The species and their corresponding data are as follows:

- (a) Atlantic croaker: 1980 (100%), 1981 (100%), 1982 (100%), 1983 (100%), 1984 (100%), 1985 (100%), 1986 (100%), 1987 (100%), 1988 (100%), 1989 (100%), 1990 (100%), 1991 (100%), 1992 (100%), 1993 (100%), 1994 (100%), 1995 (100%), 1996 (100%), 1997 (100%), 1998 (100%), 1999 (100%), 2000 (100%), 2001 (100%), 2002 (100%), 2003 (100%), 2004 (100%), 2005 (100%), 2006 (100%), 2007 (100%), 2008 (100%), 2009 (100%), 2010 (100%), 2011 (100%), 2012 (100%), 2013 (100%), 2014 (100%), 2015 (100%), 2016 (100%), 2017 (100%), 2018 (100%), 2019 (100%), 2020 (100%), 2021 (100%), 2022 (100%), 2023 (100%), 2024 (100%), 2025 (100%), 2026 (100%), 2027 (100%), 2028 (100%), 2029 (100%), 2030 (100%), 2031 (100%), 2032 (100%), 2033 (100%), 2034 (100%), 2035 (100%), 2036 (100%), 2037 (100%), 2038 (100%), 2039 (100%), 2040 (100%), 2041 (100%), 2042 (100%), 2043 (100%), 2044 (100%), 2045 (100%), 2046 (100%), 2047 (100%), 2048 (100%), 2049 (100%), 2050 (100%), 2051 (100%), 2052 (100%), 2053 (100%), 2054 (100%), 2055 (100%), 2056 (100%), 2057 (100%), 2058 (100%), 2059 (100%), 2060 (100%), 2061 (100%), 2062 (100%), 2063 (100%), 2064 (100%), 2065 (100%), 2066 (100%), 2067 (100%), 2068 (100%), 2069 (100%), 2070 (100%), 2071 (100%), 2072 (100%), 2073 (100%), 2074 (100%), 2075 (100%), 2076 (100%), 2077 (100%), 2078 (100%), 2079 (100%), 2080 (100%), 2081 (100%), 2082 (100%), 2083 (100%), 2084 (100%), 2085 (100%), 2086 (100%), 2087 (100%), 2088 (100%), 2089 (100%), 2090 (100%), 2091 (100%), 2092 (100%), 2093 (100%), 2094 (100%), 2095 (100%), 2096 (100%), 2097 (100%), 2098 (100%), 2099 (100%), 2100 (100%), 2101 (100%), 2102 (100%), 2103 (100%), 2104 (100%), 2105 (100%), 2106 (100%), 2107 (100%), 2108 (100%), 2109 (100%), 2110 (100%), 2111 (100%), 2112 (100%), 2113 (100%), 2114 (100%), 2115 (100%), 2116 (100%), 2117 (100%), 2118 (100%), 2119 (100%), 2120 (100%), 2121 (100%), 2122 (100%), 2123 (100%), 2124 (100%), 2125 (100%), 2126 (100%), 2127 (100%), 2128 (100%), 2129 (100%), 2130 (100%), 2131 (100%), 2132 (100%), 2133 (100%), 2134 (100%), 2135 (100%), 2136 (100%), 2137 (100%), 2138 (100%), 2139 (100%), 2140 (100%), 2141 (100%), 2142 (100%), 2143 (100%), 2144 (100%), 2145 (100%), 2146 (100%), 2147 (100%), 2148 (100%), 2149 (100%), 2150 (100%), 2151 (100%), 2152 (100%), 2153 (100%), 2154 (100%), 2155 (100%), 2156 (100%), 2157 (100%), 2158 (100%), 2159 (100%), 2160 (100%), 2161 (100%), 2162 (100%), 2163 (100%), 2164 (100%), 2165 (100%), 2166 (100%), 2167 (100%), 2168 (100%), 2169 (100%), 2170 (100%), 2171 (100%), 2172 (100%), 2173 (100%), 2174 (100%), 2175 (100%), 2176 (100%), 2177 (100%), 2178 (100%), 2179 (100%), 2180 (100%), 2181 (100%), 2182 (100%), 2183 (100%), 2184 (100%), 2185 (100%), 2186 (100%), 2187 (100%), 2188 (100%), 2189 (100%), 2190 (100%), 2191 (100%), 2192 (100%), 2193 (100%), 2194 (100%), 2195 (100%), 2196 (100%), 2197 (100%), 2198 (100%), 2199 (100%), 2200 (100%), 2201 (100%), 2202 (100%), 2203 (100%), 2204 (100%), 2205 (100%), 2206 (100%), 2207 (100%), 2208 (100%), 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2363 (100%), 2364 (100%), 2365 (100%),

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substrate so that said intermediate layer fills a specified space surrounding the electronic parts. In this electronic device, stresses generated by thermal expansion and differences in the thermal expansion coefficients of the aforementioned substrate, solder and resin part are alleviated by the elastic material constituting the intermediate layer.

N. Furthermore, the proposed electronic device can have a substrate in the shape of a rectangular solid with a specified thickness. If the substrate has the shape of a rectangular solid with a specified thickness, manufacture using a matrix-form collective substrate can be easily accomplished.

O. Furthermore, in the proposed electronic device, the resin part can have the shape of a rectangular solid that is formed to a specified thickness over the entire main surface of the substrate, where the side surfaces of the resin part are positioned in the same planes as the side surfaces of the substrate. This allows the electric device to be mounted with the side surfaces of the substrate and resin part facing the motherboard.

P. Furthermore, in the proposed electronic device the resin part can have the shape of a rectangular solid formed over the entire main surface of the substrate, the terminal electrodes can be embedded in the resin part, and the end surfaces of said terminal electrodes can be exposed in the same plane as at least one of the side surfaces of the resin part and/or surface of the resin part that is parallel to the main surface of the substrate. If the terminal electrodes are exposed at the side surfaces, surface mounting with the side surfaces facing the motherboard can easily be accomplished, and the direction of

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mounting can easily be confirmed as a result of the exposed positions of the terminal electrodes.

Furthermore, if the terminal electrodes are exposed at the surface that is parallel to the main surface of the substrate, the electric device can be mounted on or  
5 connected to a connector, or the like.

Q. Furthermore, in the proposed electronic device the resin part can consist of a resin in which at least one substance selected from a set consisting  
10 of a ferrite filler and a metal filler is dispersed. In this electronic device, the resin layer can be formed easily and in the desired shape at the time of manufacture; furthermore, the resin part has a heat-dissipating function and an electrostatic shielding or  
15 electromagnetic shielding function. Furthermore, in the proposed electronic device the resin part can consist of a resin which has at least one type of property selected from a set consisting of insulating properties, heat resistance, waterproof properties and  
20 chemical resistance.

R. In this electronic device, in cases where an insulating resin is used in the resin part, the main surface of the substrate and the electronic parts can be insulated from each other, and in cases where a  
25 heat-resistant resin is used in the resin part, the electronic parts can be protected from external heat. In cases where a waterproof resin is used in the resin part, the adhesion of moisture to the electronic parts is prevented by the resin part, and in cases where a  
30 chemical-resistant resin, e. g., an alkali-resistant, acid-resistant or corrosion-resistant resin, is used in the resin part, the electronic parts are protected by the resin part when such chemicals adhere to the electronic parts.

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S. Furthermore, in the proposed electronic device at least one layer selected from a set consisting of an electromagnetic field shielding layer, a heat-dissipating layer and a metal layer can be  
5 formed in a specified region on the surface of the resin part. If this is done, electromagnetic fields are blocked by the above-mentioned electromagnetic field shielding layer or metal layer, and heat  
10 dissipated by the above-mentioned heat-dissipating layer or metal layer.

T. Furthermore, according to the present invention, a resin filling method is proposed in which a specified space around electronic parts mounted on  
15 the main surface of a substrate, or the surface of an intermediate layer, is filled with a resin. This resin filling method is characterized by the fact that after at least the area around the aforementioned electronic parts is set in a vacuum state, a resin which possesses  
20 viscosity is disposed so that said resin envelops the electronic parts in the aforementioned region place. The resin is disposed so that it contacts the main surface of the substrate or surface of the intermediate layer at least in the area surrounding the  
25 aforementioned electronic parts. Then the aforementioned vacuum state is then released so that a non-vacuum state results. In this resin filling method, even if a gap should occur between the aforementioned electronic parts and the resin or  
30 between the electronic parts and the main surface of the substrate in a state in which the resin is disposed in the area surrounding the electronic parts, the interior of this gap is in a vacuum state. Accordingly, when the space outside the aforementioned

resin which is in a vacuum state is placed in a non-vacuum state, the aforementioned gap will be filled with the resin as a result of the air pressure.

Brief Description of the Drawings

5                   The above and other objects and advantages of the present invention will be apparent upon consideration of the following detailed description, taken in conjunction with the accompanying drawings, in which:

10                   Figure 1 is an external perspective view which illustrates an electronic device in a first embodiment of the present invention;

                  Figure 2 is an external perspective view which shows the above-mentioned electronic device in  
15 the first embodiment of the present invention with the resin layer removed;

                  Figure 3 is an external perspective view which shows the above-mentioned electronic device in the first embodiment of the present invention with the  
20 bottom surface of the device on top;

                  Figure 4 is a process explanatory diagram which illustrates the method used to manufacture the above-mentioned electronic device in the first embodiment of the present invention;

25                   Figure 5 is a diagram which illustrates the resin layer formation process used in the first embodiment of the present invention;

                  Figure 6 is an external perspective view which illustrates an electronic device in a second  
30 embodiment of the present invention;

                  Figure 7 is a sectional view in the direction indicated by the arrow along line A-A in Figure 6;

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Figure 8 is a process explanatory diagram which illustrates the method used to manufacture the above-mentioned electronic device in the second embodiment of the present invention;

5           Figure 9 is an external perspective view which illustrates an electronic device in a third embodiment of the present invention;

10           Figure 10 is a process explanatory diagram which illustrates the method used to manufacture the above-mentioned electronic device in the third embodiment of the present invention;

Figure 11 is an external perspective view which illustrates an electronic device in a fourth embodiment of the present invention;

15           Figure 12 is an external perspective view which shows the above-mentioned electronic device in the fourth embodiment of the present invention with the resin layer removed;

20           Figure 13 is an external perspective view which illustrates an electronic device in a fifth embodiment of the present invention;

25           Figure 14 is an external perspective view which shows the above-mentioned electronic device in the fifth embodiment of the present invention with the resin layer removed;

Figure 15 is an external perspective view which illustrates an electronic device in a sixth embodiment of the present invention;

30           Figure 16 is a sectional view in the direction indicated by the arrow along line A-A in Figure 15 (with the resin part removed);

Figure 17 is an external view which illustrates the terminal mounting surface of the

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electronic device in the sixth embodiment of the present invention;

Figure 18 is a process explanatory diagram which illustrates the method used to manufacture the above-mentioned electronic device in the sixth embodiment of the present invention;

Figure 19 is a diagram which illustrates the resin part formation method used in the sixth embodiment of the present invention; and

Figure 20 is a diagram which illustrates the resin part formation method used in the sixth embodiment of the present invention.

#### Detailed Description Of The Invention

Preferred embodiments of the present invention will be described below with reference to the drawings.

Figure 1 is an external perspective view which illustrates an electronic device in one embodiment of the present invention. Figure 2 is an external perspective view of the same with the resin layer removed. Figure 3 is an external perspective view of the same with the bottom surface on top. In the figures, 10 indicates an electronic device which has the shape of a rectangular solid with a specified thickness (e. g., a thickness of 4 mm). This electronic device 10 is constructed from a substrate 11 on which printed wiring is formed, a plurality of electronic parts 12 which are mounted on the part mounting surface (one of the main surfaces, i.e., the upper surface) of the substrate 11, a resin layer (resin part) 13 which is formed on the upper surface of the substrate 11 so that it covers the electronic parts

The substrate 11 consists of (for example) a ceramic substrate with a thickness of 1 mm whose upper surface has a rectangular shape. Lands (not shown in the figures) used for the mounting of parts are formed on the upper surface of the substrate 11, and lands 14 used for the connection of the lead terminals 15 are formed along two opposite sides of the substrate 11 on the undersurface of the substrate 11, with three lands 14 being formed on each side. Here, six lands 14 are formed, and lead terminals 15 are connected to these respective lands 14. Here, furthermore, the lead terminals 15 are soldered so that they protrude from the side surfaces of the substrate 11 in a direction perpendicular to the sides of the substrate 11. Furthermore, of the six lead terminals 15, three lead terminals are used as GND terminals, one is used as a power supply terminal, one is used as a signal input terminal, and one is used as a signal output terminal.

For example, the resin layer 13 consists of a thermosetting resin or ultraviolet-curable resin which has insulating properties, waterproof properties or heat resistance. A resin with chemical resistance, e. g., a resin with alkali resistance, acid resistance or corrosion resistance, such as a resin of the type used in batteries in order to prevent chemical changes caused by leaking of the electrolyte solution, etc., may also be used. Furthermore, a resin containing (for example) a ferrite filler may also be used.

Furthermore, the substrate 11 is not limited to a ceramic substrate; a glass-epoxy substrate, paper-epoxy substrate or paper-phenol substrate, etc., may also be used.

5 First, a collective substrate 21 in which the  
substrates 11 of a plurality of electronic devices 10  
are connected in the form of a matrix is formed  
(collective substrate manufacturing process). Here, a  
collective substrate 21 in which 16 substrates 11 are  
0 arranged in the form of a 4 × 4 matrix is formed.

Afterward, a resin layer 13 is formed on the upper surface of the collective substrate 21 using a vacuum printing method (resin layer formation process). As is shown in Figure 5, the formation of the resin layer 13 by a vacuum printing method is accomplished as follows: first, the collective substrate 21 is mounted in a stand 31 in which the collective substrate 21 can be accommodated in a horizontal state, and defoaming is performed by placing the substrate under a vacuum of 5 Torr (preparatory process). Next, a resin is supplied by printing the aforementioned resin 32 on the upper

surface of the collective substrate 21 (first printing process). In this state, bubble-form spaces are usually formed in the areas around the electronic parts 12 on the collective substrate 21.

5           Afterward, a differential pressure is generated by increasing the degree of vacuum to (e. g.) approximately 150 Torr, so that the spaces around the above-mentioned electronic parts 12 are filled with the resin (resin filling process). As a result, sinks are  
10           formed in the surface of the resin 32; accordingly, the resin 32 is again printed in a non-vacuum state (with the vacuum released) in order to fill the above-mentioned sinks (second printing process).

          Next, after the resin 32 is cured, the  
15           collective substrate 21 is removed from the stand 31, and the resin layer formation process is completed. Besides the above-mentioned resins, a resin containing a metal filler may also be used as the material of the resin layer 13 formed on the surface of the packing  
20           agent 22.

          Next, the collective substrate 21 on which the resin layer 13 has been formed is cut using a dicing device. In this case, the main bodies of the electronic devices 10 are obtained by cutting the  
25           collective substrate 21 in matrix form along the boundary lines between the individual substrates 11 (separation process). When cutting is thus performed using a dicing device, the cut surfaces are extremely smooth surfaces, so that adjustments such as flash  
30           removal, etc., can be accomplished at the same time. Furthermore, since the resin layer 13 has waterproof properties, a wet type cutting method can also be used. Here, the term "main body of the electronic device 10"

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refers to an electronic device on which lead terminals 15 have not yet been mounted.

Afterward, the lead terminals 15 are connected to the main bodies of the electronic devices 10, thus completing the electronic devices 10. It is desirable that the connection of the lands 14 of the substrate 11 and the lead terminals 15 be accomplished using a high-melting-point solder. It is sufficient if the melting point of this high-melting-point solder is higher than the melting point of the solder used for the mounting of the electronic device 10 on the motherboard. The use of such a high-melting-point solder ensures that the lead terminals 15 will not come loose during the soldering of the electronic device 10 to the motherboard, so that unsatisfactory connections can be prevented.

Since a collective substrate 21 in which a plurality of substrates are connected in the form of a matrix is used in the manufacturing method of the aforementioned first embodiment, the waste of substrate materials seen in conventional examples can be greatly reduced.

Furthermore, since the resin layer 13 is formed in the state of the aforementioned collective substrate 21, and since adjustments such as flash removal and the like, can be performed simultaneously with the separation of the collective substrate 21, the number of steps required can be reduced compared to when manufacture is performed using individual substrates instead of a collective substrate.

Furthermore, in cases where the resin layer 13 is formed using a transfer molding technique, which is a universally known resin sealing technique, a number of drawbacks are encountered: e. g., a mold is required,

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a pressing machine is required, flashes are formed on the sealed products, and air is entrapped so that voids (bubbles) tend to be formed during sealing. However, all of these drawbacks can be eliminated by using a vacuum printing technique.

Furthermore, since the resin layer 13 is formed by a vacuum printing method, the resin layer 13 can be formed without any gaps in the areas surrounding the electronic parts 12, so that the durability of the electronic device 10 can be increased.

Moreover, since the resin layer 13 is formed by a vacuum printing method, the surface of the resin layer 13 can be formed as a flat surface; accordingly, suction chucking by means of an automatic mounting machine can be easily accomplished.

Furthermore, in cases where packing is applied, the insulation resistance can be increased even in cases where the resin layer 13 is formed using a resin that contains a metal filler.

Moreover, in the packing process, the above-mentioned packing agent 22 may be sprayed or applied with a brush, etc.; alternatively a film may be pasted to the surface. Furthermore, sufficient durability can be obtained even if a coating of the packing agent 22 is not applied.

Furthermore, in the embodiment described above, a dicing device was used to cut the collective substrate. However, the present invention is not limited to this; it would also be possible to cut the collective substrate into respective substrates by means of a laser, water or wire, etc. In this case, the shape of the substrate may be round, triangular or some other polygonal shape, or the shape may be

flexibly adjusted to match the case that accommodates the electronic device of the present invention.

Furthermore, in this embodiment, there are no particular limits on the function of the electronic device 10; the present invention can be applied to various types of electronic devices. For example, the present invention can be applied to manufacture of electronic devices including high-frequency power amplifiers, electronic volumes, DC/DC converters, FET switches, low-power telemeters, keyless transmitters and inverters, etc.

Next, a second embodiment of the present invention will be described.

Figure 6 is an external perspective view of an electronic device in a second embodiment of the present invention, and Figure 7 is a sectional view in the direction indicated by the arrow along line A-A in Figure 6.

The intermediate layer 16 consists of an elastic material which has insulating properties, such as silicon or a rubber, etc. This intermediate layer 16 is formed so that it covers the upper surface of the substrate 11 and the surfaces of the electronic parts 12.

Next, the method used to manufacture the above-mentioned electronic device 10 will be described with reference to the explanatory diagrams shown in Figure 8.

First, a collective substrate 21 in which the substrates 11 of a plurality of electronic devices 10 are connected is formed (collective substrate manufacturing process). Here, a collective substrate 21 in which 16 substrates 11 are connected in the form of a 4 × 4 matrix is formed.

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Next, after the electronic parts 12 have been mounted on the upper surface of this collective substrate 21 (electronic part mounting process), an intermediate layer 16 is formed by coating the upper  
5 surface of the collective substrate 21 with an insulating elastic material so that the electronic parts 12 are covered (intermediate layer formation process). Here, the intermediate layer 16 is applied as a coating for purposes of insulation, waterproofing  
10 and protection, and especially for the purpose of alleviating stresses generated by thermal expansion and differences in the thermal expansion coefficients of the substrate 11, solder and resin layer 13. Examples of insulating elastic materials that can be used  
15 include acrylic type, urethane type, silicone type, fluorine type, rubber type, vinyl type, polyester type, phenol type, epoxy type, and wax type coating materials, etc.

Afterward, in the same manner as in the first  
20 embodiment, the resin layer 13 is formed and the collective substrate 21 is cut using a dicing device, after which the electronic devices 10 are completed by connecting lead terminals 15.

In the electronic devices 10 thus obtained,  
25 an intermediate layer 16 consisting of an insulating elastic material is disposed between (a) the substrate 11 and electronic parts 12, and (b) the resin layer 13. Accordingly, stresses generated as a result of the thermal expansion of the substrate 11, solder and resin  
30 layer 13, and as a result of differences in the thermal expansion coefficients of these parts, can be alleviated. Specifically, during the process (reflow, etc.) in which the motherboard and electronic device 10 are connected, movement and outflow of the solder can

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be prevented, and the generation of cracks in the electronic parts 12 can be prevented.

Furthermore, in the intermediate layer formation process, a vacuum printing method may be  
5 used, or liquid-form elastic materials may be applied by spraying or coating with a brush, etc.

Next, a third embodiment of the present invention will be described.

The manufacturing method used in this third  
10 embodiment is as follows:

Figure 9 is an external perspective view which illustrates an electronic device 40 in the third embodiment. In this figure, constituent parts which are the same as in the aforementioned first embodiment  
15 are labeled with the same symbols, and a description of these parts is omitted. Furthermore, the main point of difference between this third embodiment and the first is that a metal layer 17 is formed on the upper surface of the resin layer 13. By thus forming a metal layer  
20 17, it is possible to obtain an electromagnetic field shielding effect, so that an effect is exhibited in terms of EMC (electromagnetic compatibility) countermeasures.

The above-mentioned electronic device 40 is  
25 manufactured as follows:

Specifically, as is shown in Figure 10, a collective substrate 21 in which the substrates 11 of a plurality of electronic devices 40 are connected is formed (collective substrate manufacturing process).  
30 After electronic parts 12 are mounted on the upper surface of the collective substrate 21 (electronic part mounting process), a packing agent 22 is applied to the upper surface of the collective substrate 21 as a

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coating so that this packing agent 22 covers the electronic parts 12 (packing process).

Afterward, a resin layer 13 is formed on the upper surface of the collective substrate 21 using a vacuum printing method similar to that described above (resin layer formation process).

Furthermore, a metal layer 17 is formed on the upper surface of the resin layer 13 (metal layer formation process). Here, a metal layer 17 is formed on the upper surface of the resin layer 13 by coating the upper surface of the resin layer 13 with a resin in which a metal filler is dispersed, and then curing this resin.

Next, the collective substrate 21 on which the metal layer 17 has been formed is cut using a dicing device, etc. In this case, the main bodies of the electronic devices 40 are obtained by cutting the collective substrate 21 in matrix form along the boundary lines between the individual substrates 11 (separation process). Here, the term "main body of the electronic device 40" refers to an electronic device on which lead terminals 15 have not yet been mounted.

When cutting is thus performed using a dicing device, the cut surfaces are extremely smooth surfaces, so that adjustments such as flash removal, etc., can be accomplished at the same time. Furthermore, since the resin layer 13 has waterproof properties, a wet-type cutting method can also be used.

Afterward, the lead terminals 15 are connected to the main bodies of the electronic devices 40, thus completing the electronic devices 40.

Furthermore, in the formation of the metal layer 17, it would also be possible to paste a metal film to the surface, or to use an ordinary metal film

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formation technique. Furthermore, a resin containing a metal filler may also be formed as a film.

Next, a fourth embodiment of the present invention will be described.

5           Figure 11 is an external perspective view which illustrates an electronic device 50 in a fourth preferred embodiment. Figure 12 is an external perspective view of the same electronic device with the resin layer removed. In the figures, constituent parts  
10           which are the same as in the aforementioned first embodiment are labeled with the same symbols, and a description of these parts is omitted. Furthermore, the main point of difference between the fourth  
15           embodiment and the first embodiment is that terminal electrodes 51 which are exposed at the side surfaces of the resin layer 13 are formed instead of the lead  
            terminals 15 used in the first preferred embodiment.

            Specifically, a substrate 11' in which no lands for the connection of lead terminals are formed  
20           on the undersurface, but in which lands 14' for the connection of terminal electrodes are formed on the upper surface (part mounting surface), is used instead of the substrate 11 used in the first embodiment .  
            Four of these lands 14' are formed along one long side  
25           of the substrate 11', and a cylindrical terminal electrode 51 is soldered to each of the lands 14'.  
            Furthermore, one end surface of each terminal electrode 14' is positioned in the plane that includes the side surface of the substrate 11' and the side surface of  
30           the resin layer 13, so that this end surface is exposed to the outside. Here, of the four terminal electrodes 51, one electrode is used as a GND electrode, one is used as a power supply electrode, one is used as a

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signal input electrode, and one is used as a signal output electrode.

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The electronic device 50 constructed as described above can be mounted with the side surfaces  
5 of the substrate 11' and resin layer 13 facing the motherboard, so that the required mounting area can be reduced and high-density mounting can be realized. Furthermore, as was described earlier, the mounting of the electronic device on the motherboard by means of an  
10 automatic mounting machine can also easily be accomplished. Moreover, since the terminal electrodes 51 are exposed only on one side surface of the substrate 11', the mounting direction can easily be confirmed on the basis of these exposed positions.

15       Next, a fifth embodiment of the present invention will be described.

Figure 13 is an external perspective view which illustrates an electronic device 60 in a fifth embodiment of the present invention. Figure 14 is an  
20 external perspective view which shows the same electronic device with the intermediate layer and resin layer removed. In the figures, constituent parts which are the same as in the aforementioned fourth embodiment are labeled with the same symbols, and a description of  
25 these parts is omitted. Furthermore, the main point of difference between the fifth embodiment and the fourth embodiment is that in the fifth preferred embodiment, terminal electrodes 54 in which through-hole conductors 55 and metal plates 57 are exposed at the side surfaces  
30 of the resin layer 13 and intermediate layer 16 are installed instead of the terminal electrodes 51 used in the fourth preferred embodiment.

Specifically, a substrate 61 in which through-hole conductors 55 that are cut in half are

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exposed at one side surface is used instead of the substrate 11' used in the fourth preferred embodiment. Four of these through-hole conductors 55 are formed along one long side of the substrate 61, and a metal plate 57 is soldered onto the upper opening of each through-hole conductor 55 so that this plate 57 covers the opening. Furthermore, the interior of the recessed part of each through-hole conductor 55 may be filled with solder 56. Moreover, the through-hole conductors 55, solder 56 and metal plates 57 are positioned in the plane that includes the side surface of the substrate 61 and the side surfaces of the resin layer 13 and intermediate layer 16, so that said through-hole conductors, solder and metal plates are exposed to the outside. Here, of the four terminal electrodes 54, one electrode is a GND electrode, one is a power supply electrode, one is a signal input electrode, and one is a signal output electrode.

As in the case of the fourth preferred embodiment, the electronic device 60 constructed as described above can be mounted with the side surfaces of the substrate 61, resin layer 13 and intermediate layer 16 facing the motherboard, so that the required mounting area can be reduced and high-density mounting can be realized. Furthermore, as was described earlier, the mounting of the electronic device on the motherboard by means of an automatic mounting machine can also easily be accomplished. Moreover, since the terminal electrodes 54 are exposed only on one side surface of the substrate 61, the mounting direction can easily be confirmed on the basis of these exposed positions.

Next, a sixth embodiment of the present invention will be described.

Figure 15 is an external view which illustrates an electronic device 80 in a sixth embodiment of the present invention. Figure 16 is a side view in the direction indicated by the arrow along line A-A in Figure 15 (with the resin part removed). Figure 17 is an external view which shows the terminal mounting surface. In the figures, constituent parts which are the same as in the aforementioned first embodiment are labeled with the same symbols, and a description of these parts is omitted. Furthermore, the main point of difference between the sixth embodiment and the first embodiment is that in the sixth preferred embodiment, a substrate 81 on which a face-down bonding type electronic part 83 that has numerous connecting terminals is mounted is formed, and a resin part 84 is formed by filling only a specified space surrounding this electronic part 83 with a resin.

Specifically, as is shown in Figure 16, the face-down bonding type electronic part 83 has a plurality of connecting terminals located between said electronic part 83 and the main surface of the substrate 81; accordingly, a gap 85 is formed between substrate 81 and the electronic part 83. As a result, these connecting terminals are susceptible to short-circuiting, and the mounting strength of the electronic part 83 is low. The above-mentioned resin part 84 is formed only for this electronic part 83, so that the above-mentioned short-circuiting is prevented, and the mounting strength is increased. In the present embodiment, the gap between the substrate 81 and the electronic part 83 is completely filled with the resin.

Next, one example of a method for manufacturing the above-mentioned electronic device 80

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will be described with reference to Figures 18 through 20.

As is shown in Figure 18, a collective substrate 86 in which the substrates 81 of a plurality of electronic devices 80 are connected in the form of a matrix is formed (collective substrate manufacturing process), and electronic parts 12 and 83 are mounted on the upper surface of the collective substrate 86 (electronic part mounting process).

Afterward, the resin part 84 is formed in the area surrounding the electronic part 83 (resin part formation process). In this resin part formation process, as is shown in Figure 18, the area surrounding the electronic part 83 is first placed in a vacuum state, and a resin 87 that has viscosity is disposed so that it envelops the electronic part 83. In this case, it is desirable that the resin 87 be disposed so that no gap is generated between the substrate 81 and the resin 87.

In this state, the gap 85 between the substrate 81 and the electronic part 83 is not filled with the resin 87. Furthermore, a space that is not filled with the resin 87 exists around the electronic part 83.

Next, when the surrounding area is placed in a non-vacuum state, the gap 85 and space surrounding the electronic part 83 is filled with the resin 87 as a result of the air pressure surrounding the resin 87. Afterward, the resin part 84 is formed by curing the resin 87.

Next, the collective substrate 86 on which the resin parts 84 have been formed is cut using a dicing device, etc. In this case, the main bodies of the electronic devices 80 are obtained by cutting the

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Although preferred illustrative embodiments of the present invention are described above, it will be evident to one skilled in the art that various



changes and modifications may be made without departing from the invention. The respective embodiments described above are concrete examples of the present invention; the present invention is not limited to these examples alone. The present invention includes electronic devices and electronic device manufacturing methods in which the constitutions described in the above-mentioned preferred embodiments are combined or used separately. For example, in cases where the terminal electrodes are disposed as in the fourth embodiment (see Figures 11 and 12), it would also be possible to form a resin layer only in the areas covering the electrode terminals instead of forming a resin layer over the entire main surface of the substrate. Even in the case of such an arrangement, the electronic device can be mounted on the motherboard with the end surfaces of the electrode terminals as parts of the bottom surface.. It is intended in the appended claims to cover all such changes and modifications that fall within the true spirit and scope of the invention.

Furthermore, even in cases where an electronic device on which no resin layer or resin part is formed is manufactured, the waste of substrate material can be eliminated and a reduction in cost can be achieved by using the above-mentioned manufacturing method.

In the electronic device manufacturing method of the present invention presented in paragraphs A through K of the summary of the invention above, electronic devices are obtained by using a collective substrate in which a plurality of substrates are connected in the form of a matrix, and then ultimately separating this collective substrate. Accordingly, the

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waste of substrate material can be greatly reduced. Furthermore, the separation of the collective substrate and adjustments such as flash removal, etc., can be simultaneously performed. Moreover, since a resin part

5 that covers the electronic parts is formed in the state of the collective substrate, the number of steps required can be reduced compared to cases where electronic devices are manufactured using individual substrates instead of a collective substrate.

10 Moreover, since an intermediate layer consisting of an insulating elastic material is formed, stresses generated as a result of the thermal expansion of the substrate, solder and resin layer, and as a result of differences in the thermal expansion coefficients of

15 these parts, can be alleviated. Accordingly, during the process (reflow, etc.) in which the motherboard and electronic device are connected, the movement and outflow of solder can be prevented, and the generation of cracks in the electronic parts can be prevented.

20 Moreover, by forming the aforementioned resin part by means of a vacuum printing method, it is possible to form the surface of the resin part as a flat surface, so that suction chucking by means of an automatic mounting machine is easy.

25 Furthermore, in the case of the electronic device presented in paragraphs L through S of the summary of the invention above, the manufacturing method presented in the aforementioned paragraphs A through K of the summary of the invention can easily be

30 applied. Moreover, high-density mounting can easily be accomplished, and mounting on motherboards by means of an automatic mounting machine is also possible.

Furthermore, in the case of the resin filling method presented in paragraph T of the summary of the

invention above, it is possible to fill only a specified space surrounding the desired electronic part with a resin, without forming any gaps.

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We claim:

separating said collective substrate into individual electronic devices.

cutting said terminal electrode members as said collective substrate is separated into individual electronic devices.

3. The method of Claim 1 further comprising forming an intermediate layer consisting of an insulating elastic material on the main surface of said collective substrate prior to the formation of said resin part, so that said intermediate layer covers said electronic parts.

4. The method of Claim 2 further comprising forming an intermediate layer consisting of an insulating elastic material on the main surface of said collective substrate prior to the formation of said resin part, so that said intermediate layer covers said electronic parts.

5. The method of Claims 1, 2, 3 or 4 further comprising forming said resin part with a specified thickness over the entire main surface of said collective substrate using a vacuum printing method to form said resin part.

6. The method of Claim 5 further comprising forming a further resin layer in either a non-vacuum state or a vacuum state on the surface of said resin part after said resin part has been formed using said vacuum printing method.

7. The method of Claims 1, 2, 3 or 4 further comprising forming at least one of a heat-dissipating layer, an electromagnetic field shielding layer or a metal layer in a specified region on the surface of said resin part following the formation of said resin part.

8. The method of Claim 7, further comprising forming an electromagnetic field shielding layer using a resin in which at least one of a ferrite filler or a metal filler is dispersed.

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9. The method of Claims 1, 2, 3 or 4 wherein said resin part is formed using an insulating resin.

10. The method of Claims 1, 2, 3 or 4 wherein said resin part is formed using a resin in which at least one ferrite filler or a metal filler is dispersed.

11. The method of Claims 1, 2, 3 or 4 wherein said resin part is formed using a material that has waterproof properties.

12. The method of Claims 1 or 3 wherein forming said resin part comprises forming said resin part with a specified thickness over the entire main surface of said collective substrate and

separating said collective substrate into individual electronic devices comprises cutting said collective substrate using a dicing device.

13. The method of Claims 2 or 4 wherein separating said collective substrate into individual electronic devices comprises cutting said collective substrate using a dicing device.

14. An electronic device comprising a substrate, electronic parts that are mounted on the main surface of said substrate, a resin part that is formed on the main surface of said substrate so that said resin part fills a specified space surrounding at least one of said electronic parts, and terminal electrodes that are exposed to the outside.

15. The electronic device of Claim 14, with an intermediate layer consisting of an insulating elastic material formed on the main surface of said substrate so that said intermediate layer fills a specified space surrounding said electronic parts.

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21. The electronic device of Claim 14 or Claim 15, with said at least one layer selected of an electromagnetic field shielding layer, a heat-dissipating layer or a metal layer formed in a specified region on the surface of said resin part.

22. A resin filling method in which a specified space around at least one electronic part mounted on the main surface of a substrate, or the surface of an intermediate layer, is filled with a resin, said resin filling method comprising

setting at least the area around said electronic parts in a vacuum state,

disposing a resin which possesses viscosity so that said resin envelops at least one of said electronic parts in said region placed in a vacuum state, and so that said resin contacts the main surface of said substrate or surface of said intermediate layer at least in the area surrounding said at least one of said electronic parts, and

releasing said vacuum state so that a non-vacuum state results.

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ABSTRACT

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The present invention provides an electronic device manufacturing method and an electronic device which make it possible to reduce the waste of materials and the number of manufacturing steps required. Electronic devices are manufactured via a process including creating a collective substrate, in which a plurality of substrates corresponding to the electronic devices being manufactured are connected in the form of a matrix, mounting electronic parts on the upper surface of the collective substrate, forming a solidified resin layer using a vacuum printing method so that said resin layer covers the upper surface of the collective substrate on which the aforementioned parts have been mounted, or an intermediate layer consisting of an insulating elastic material, and so that said resin layer covers the electronic parts, and separating the collective substrate on which the above-mentioned resin layer has been formed into individual substrates. Since a collective substrate is used and electronic devices are obtained by ultimately separating this collective substrate, the waste of substrate material can be greatly reduced, and the number of manufacturing steps required can also be reduced.

Figure 1

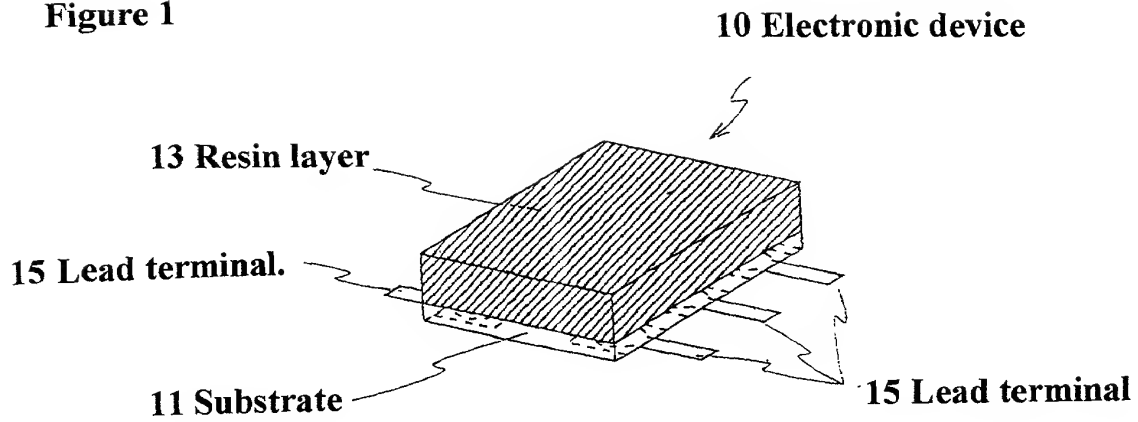


Figure 2

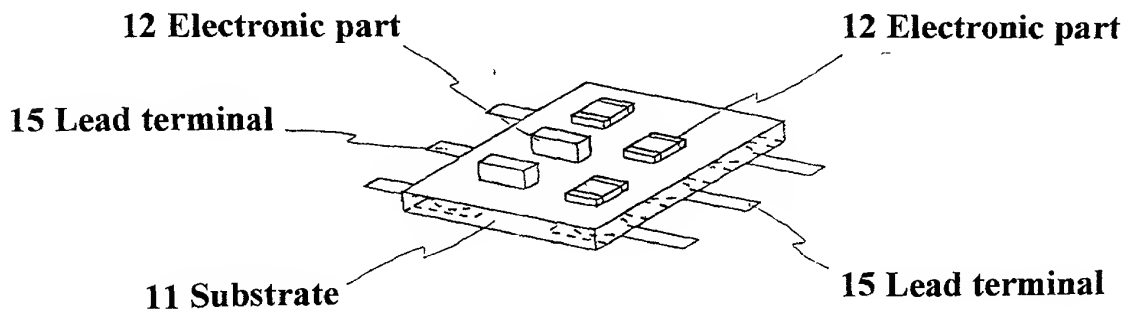


Figure 3

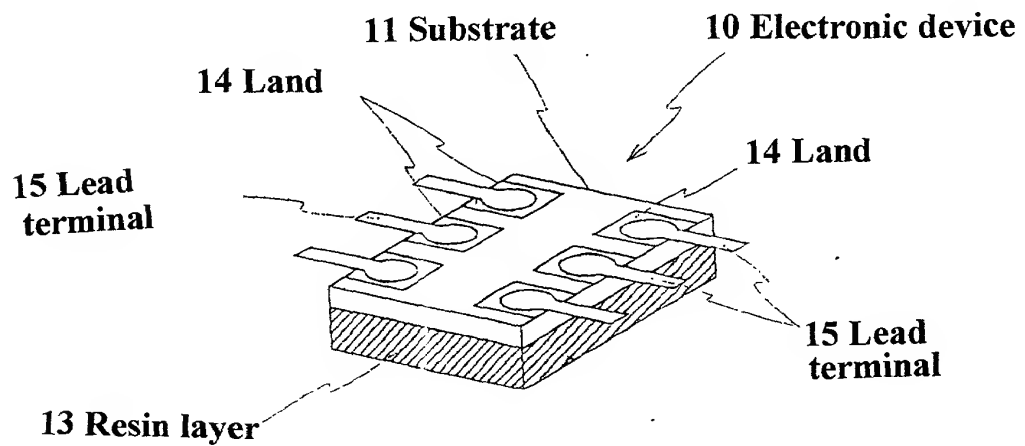


Figure 4

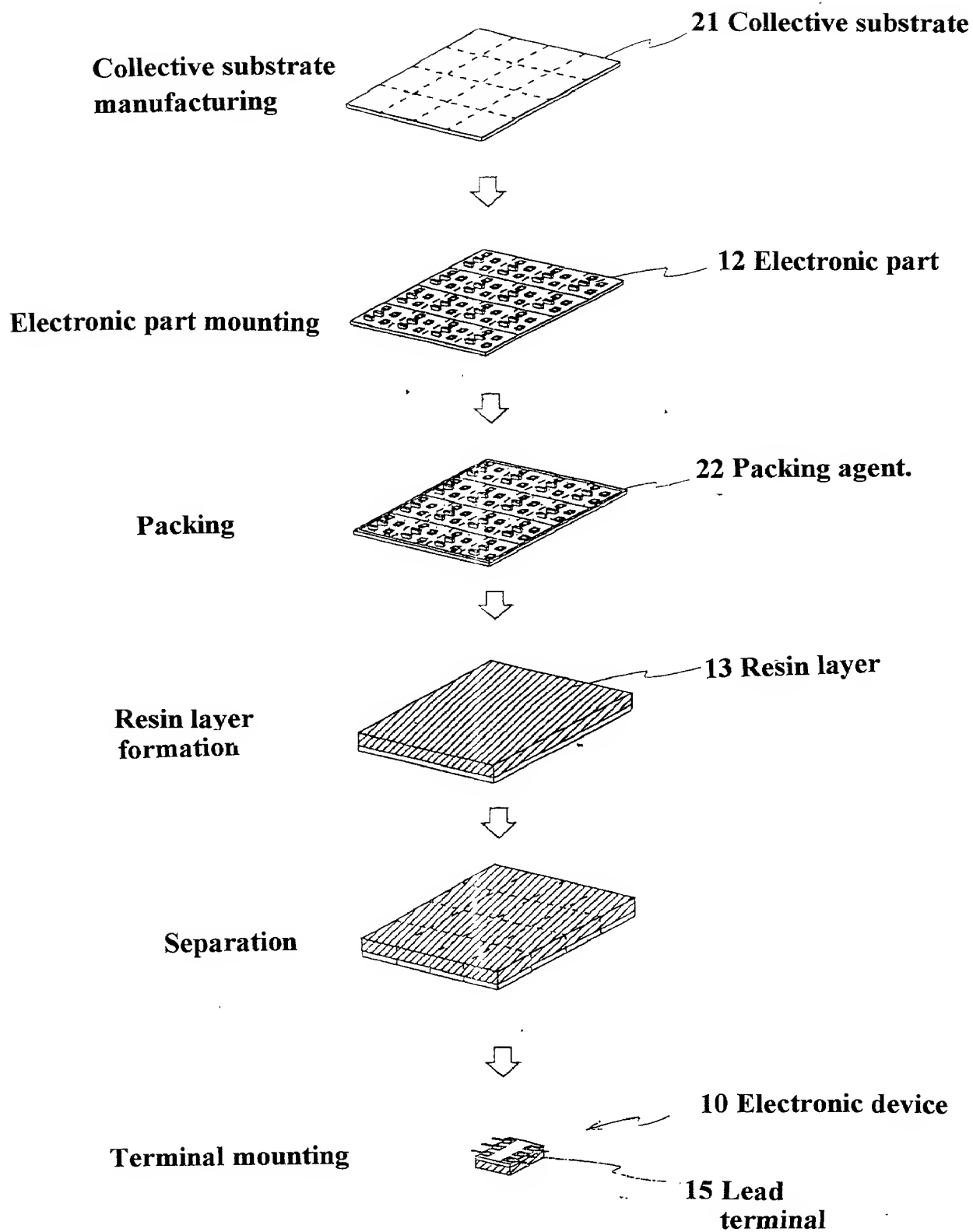
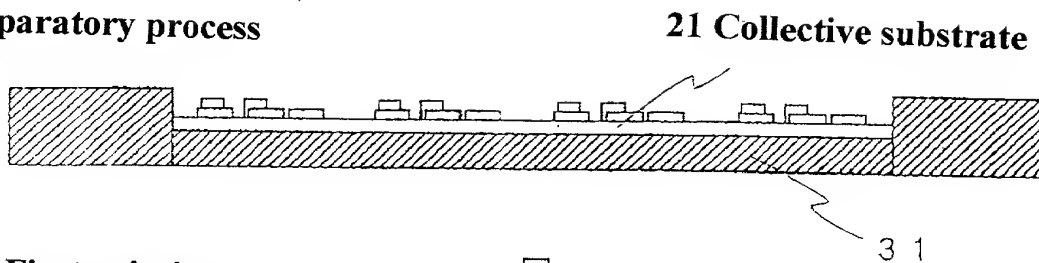
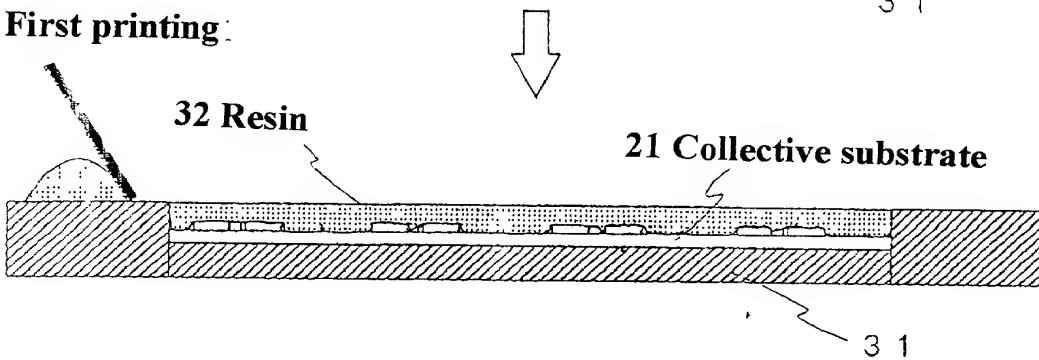


Figure 5

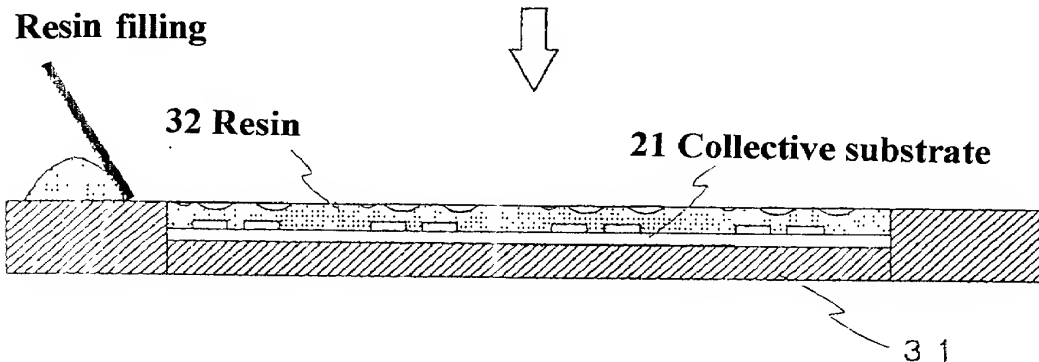
Preparatory process



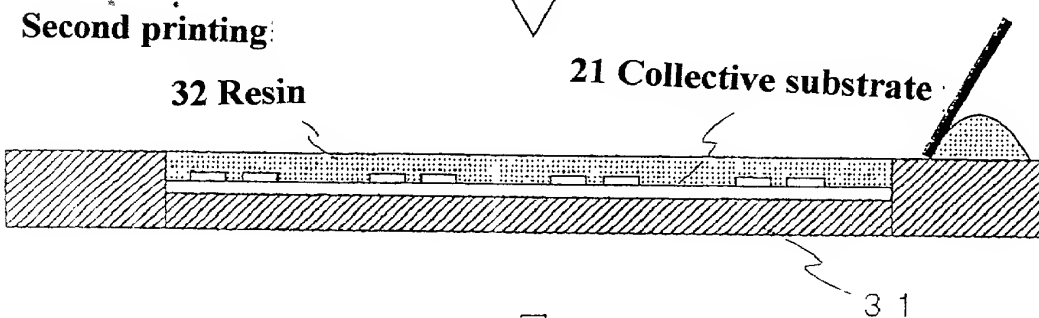
First printing:



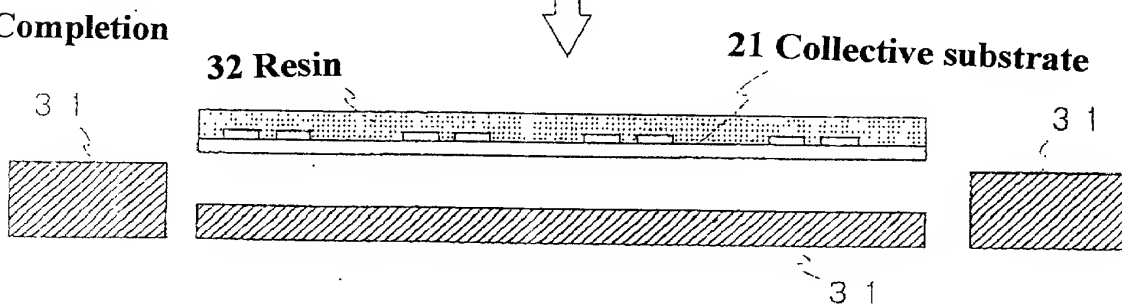
Resin filling



Second printing:



Completion



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Figure 6

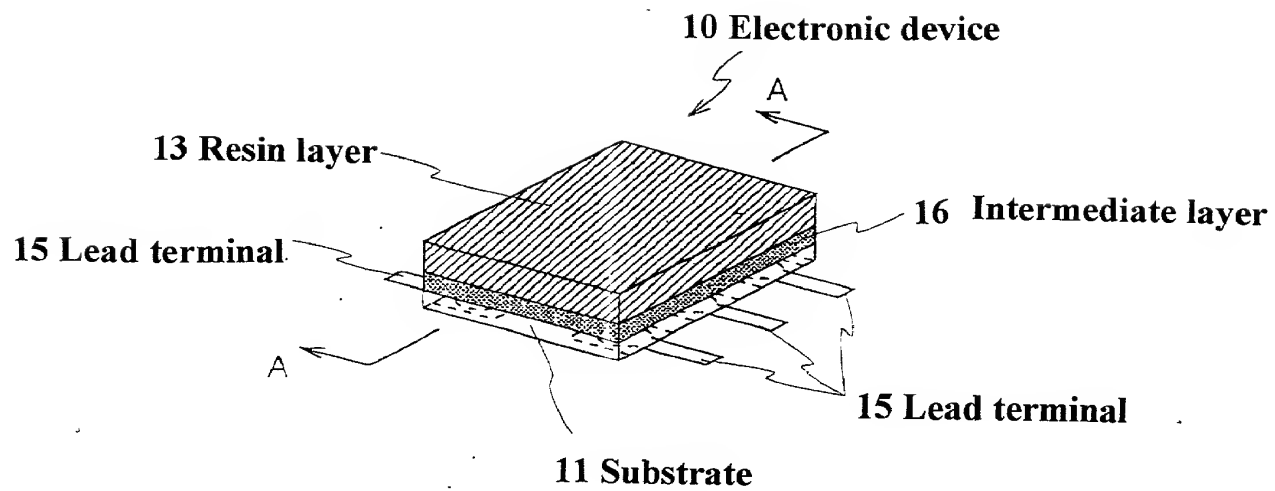


Figure 7

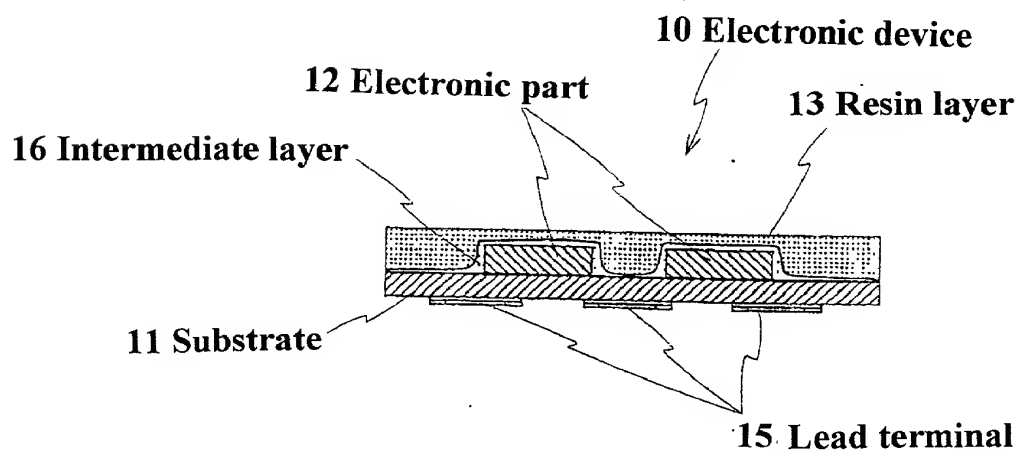


Figure 8

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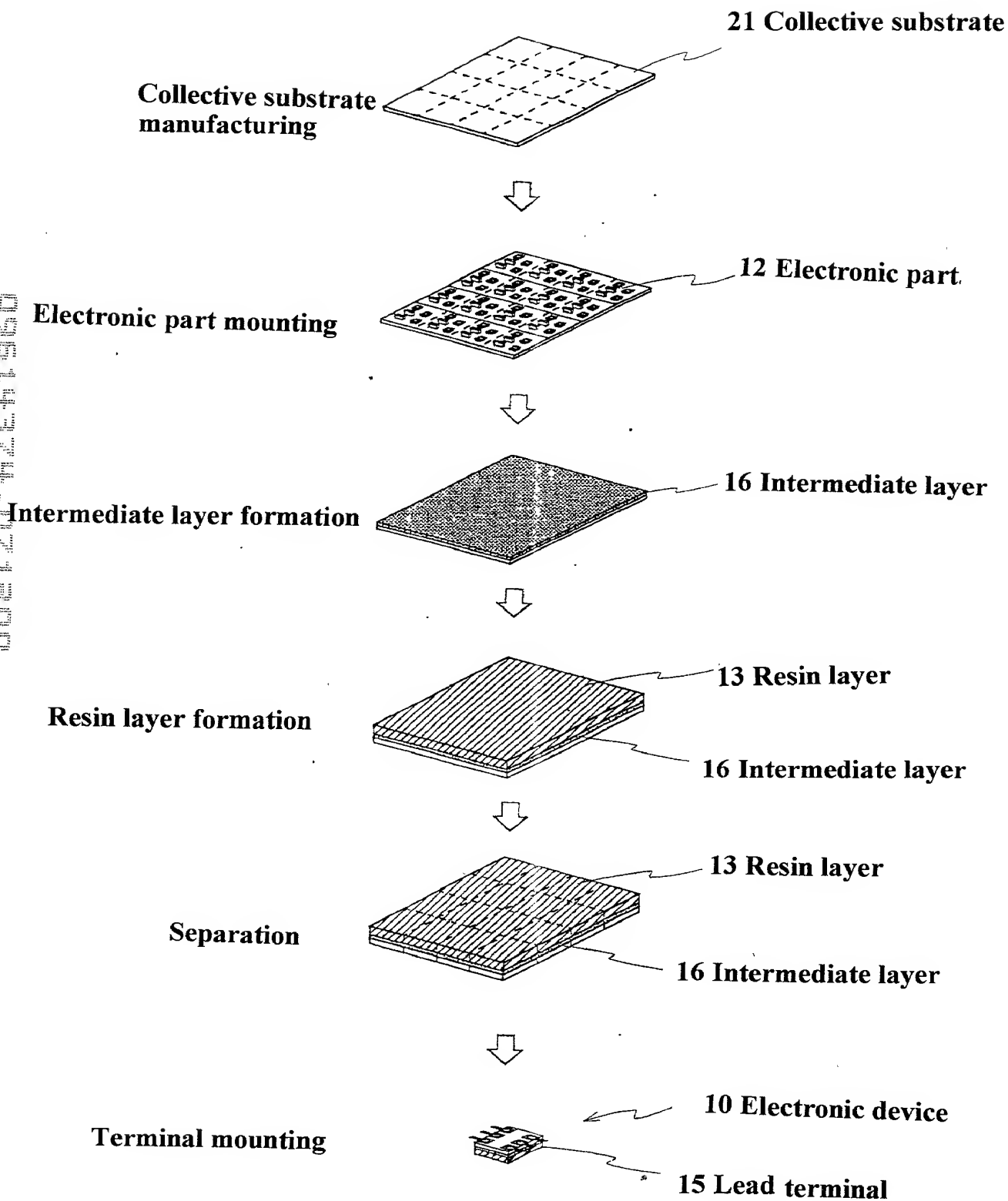
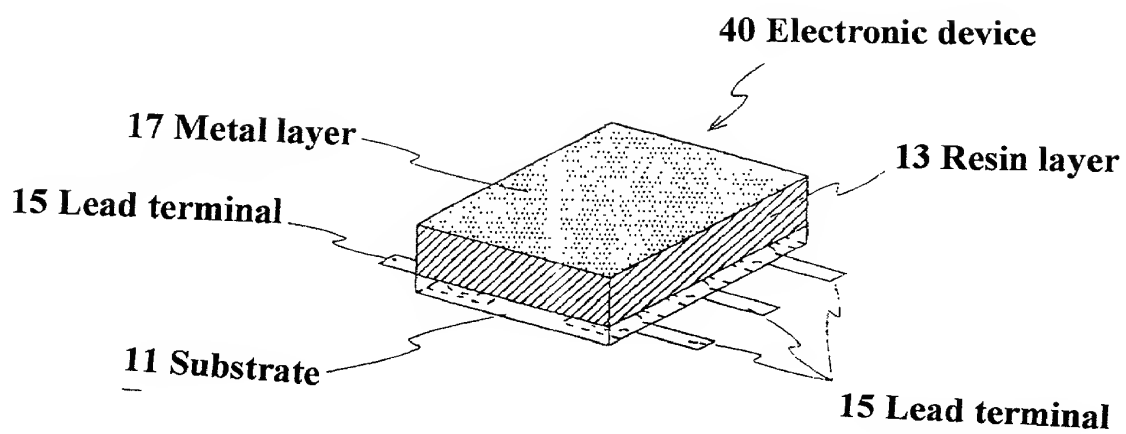


Figure 9



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Figure 10

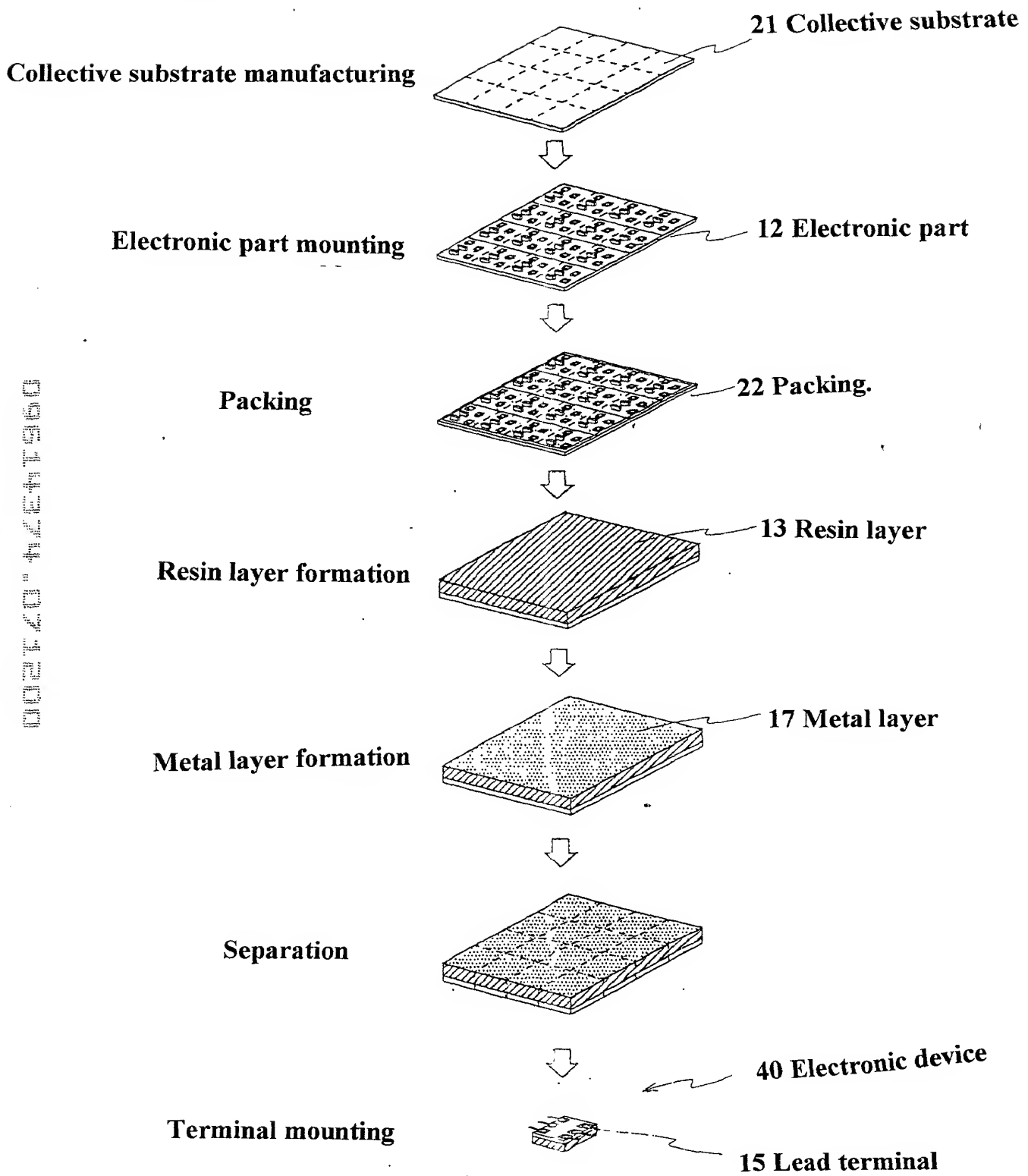




Figure 11

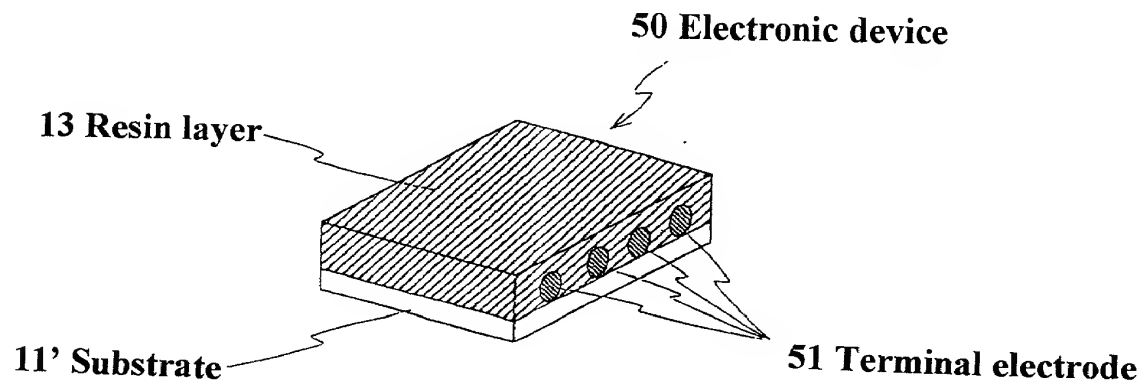
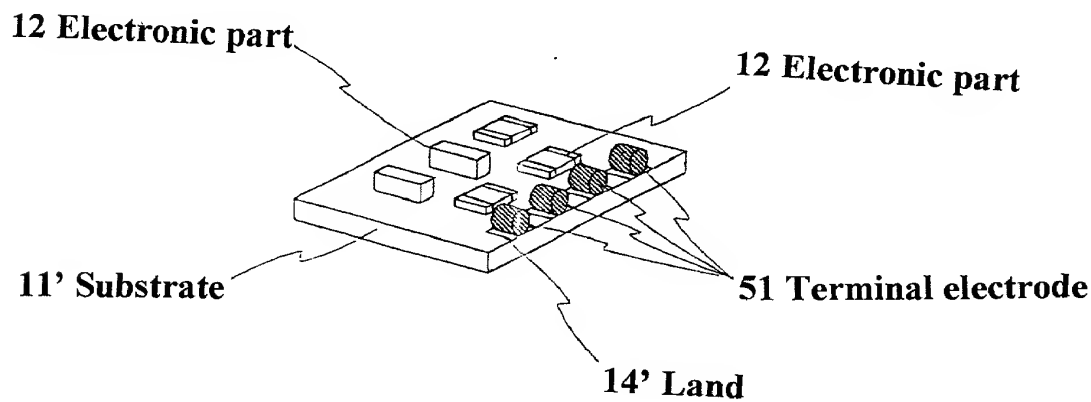


Figure 12



# Key to Figure 13

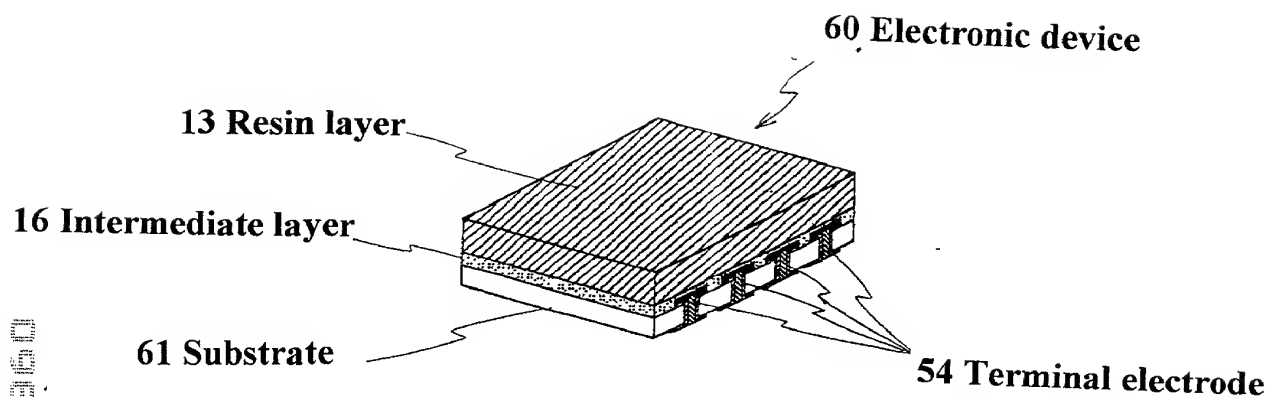


Figure 14

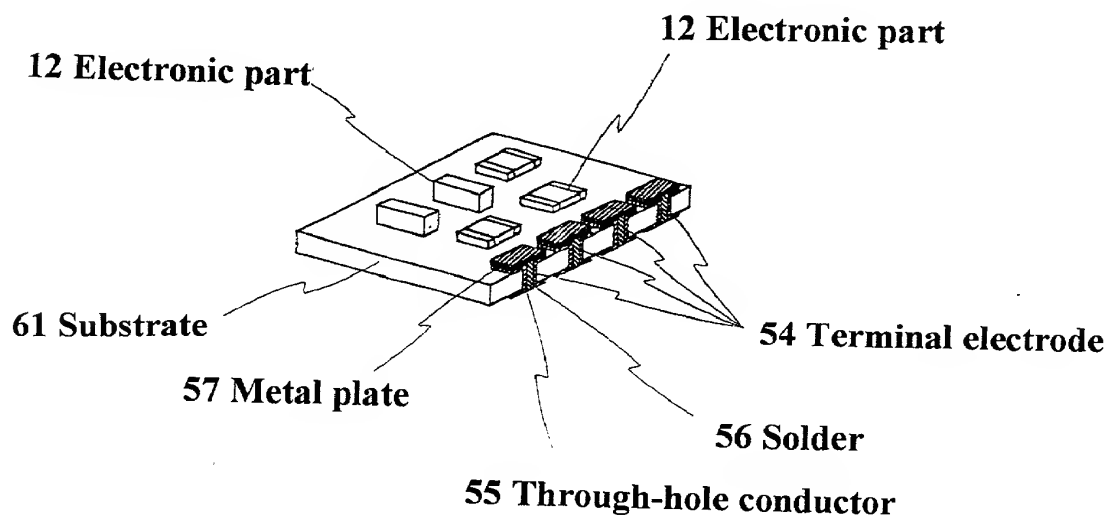


Figure 15

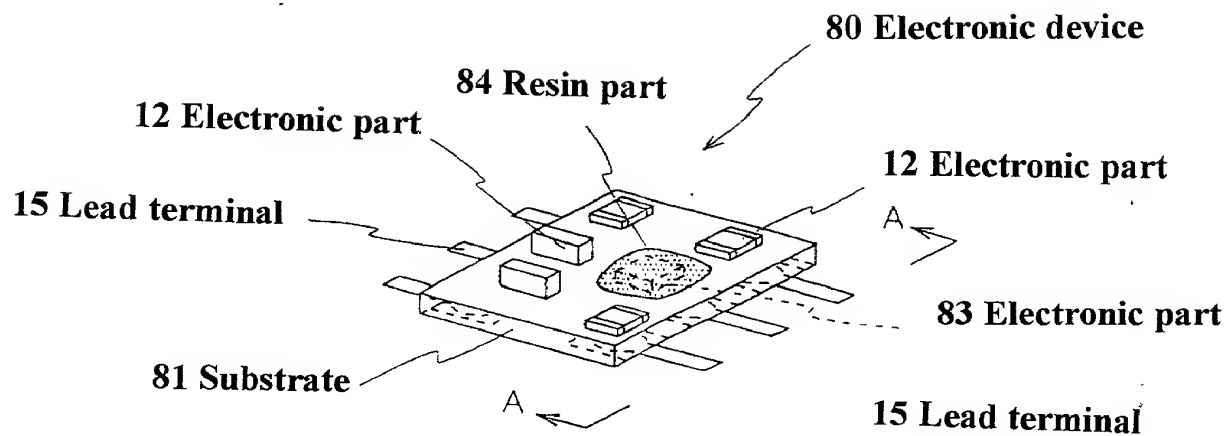


Figure 16

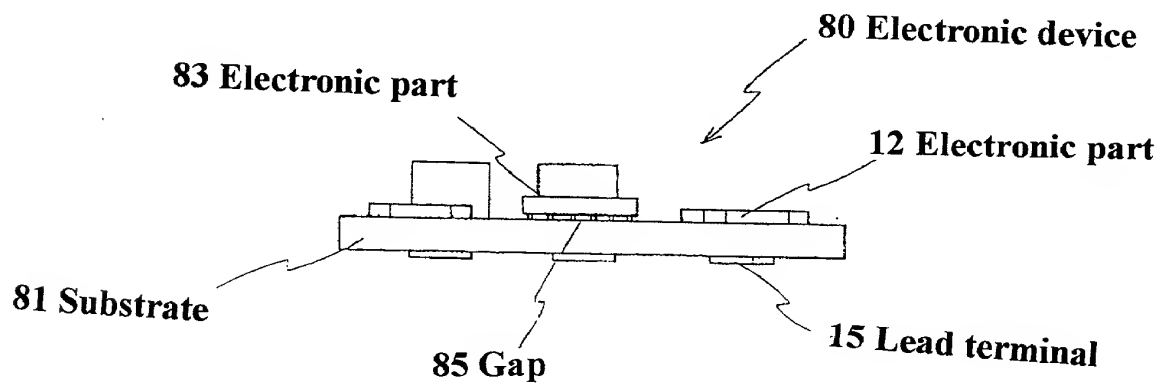


Figure 17

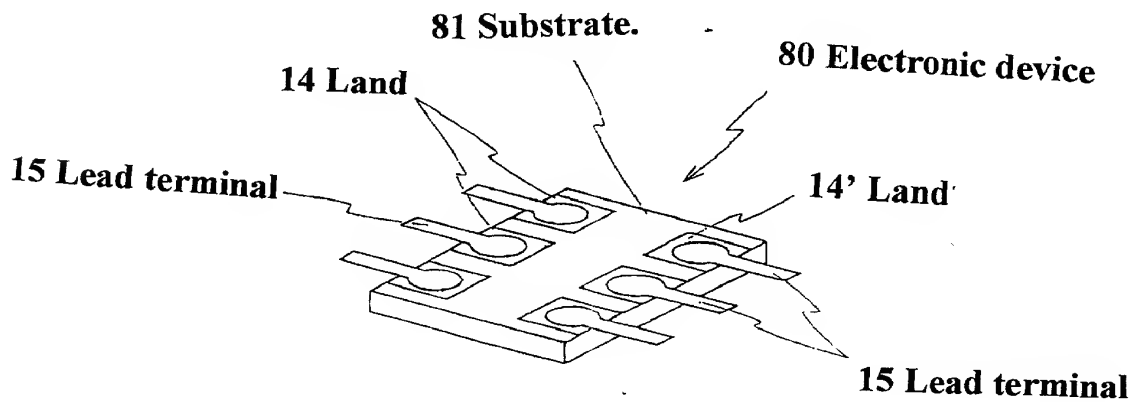


Figure 18

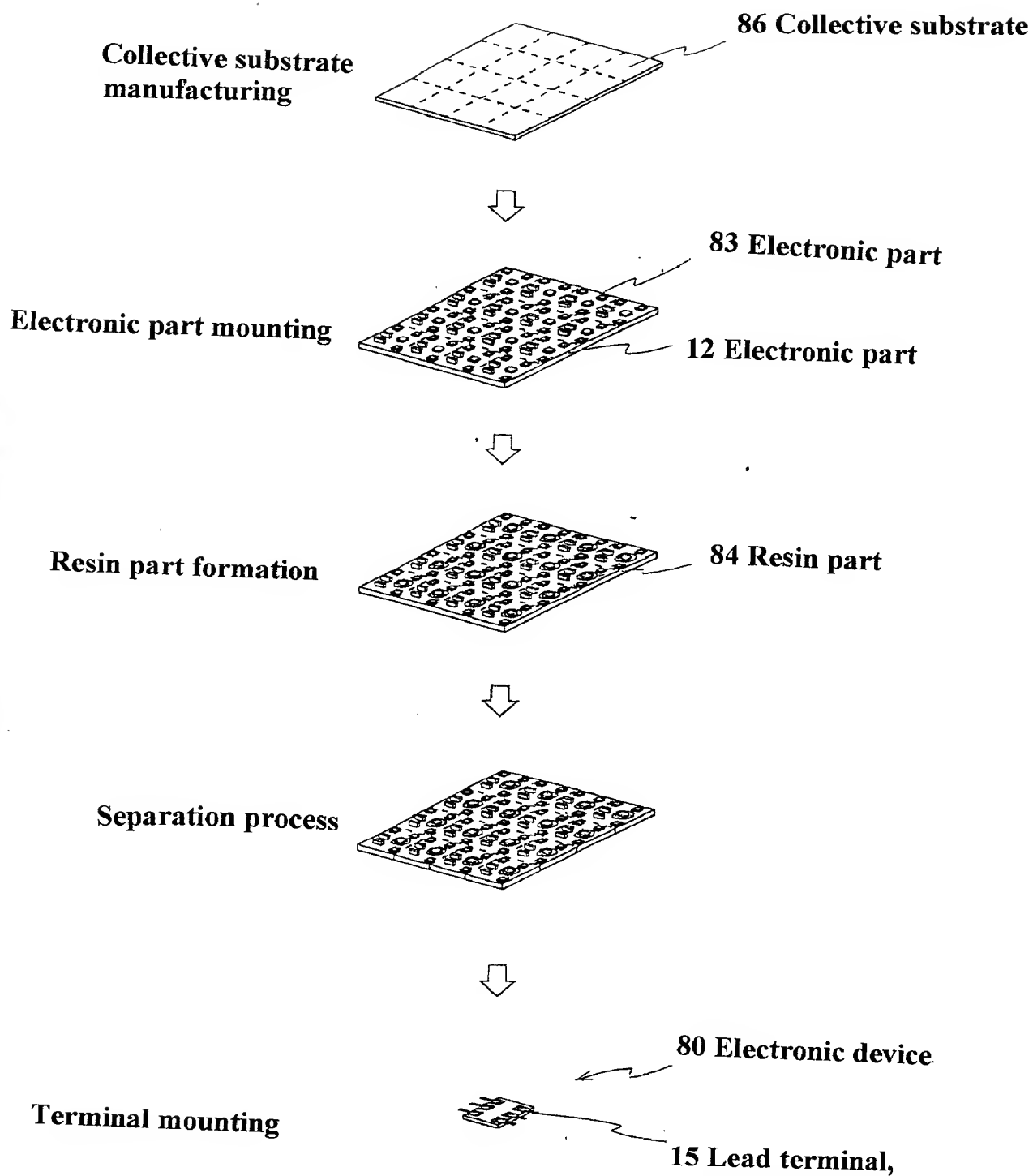


Figure 19

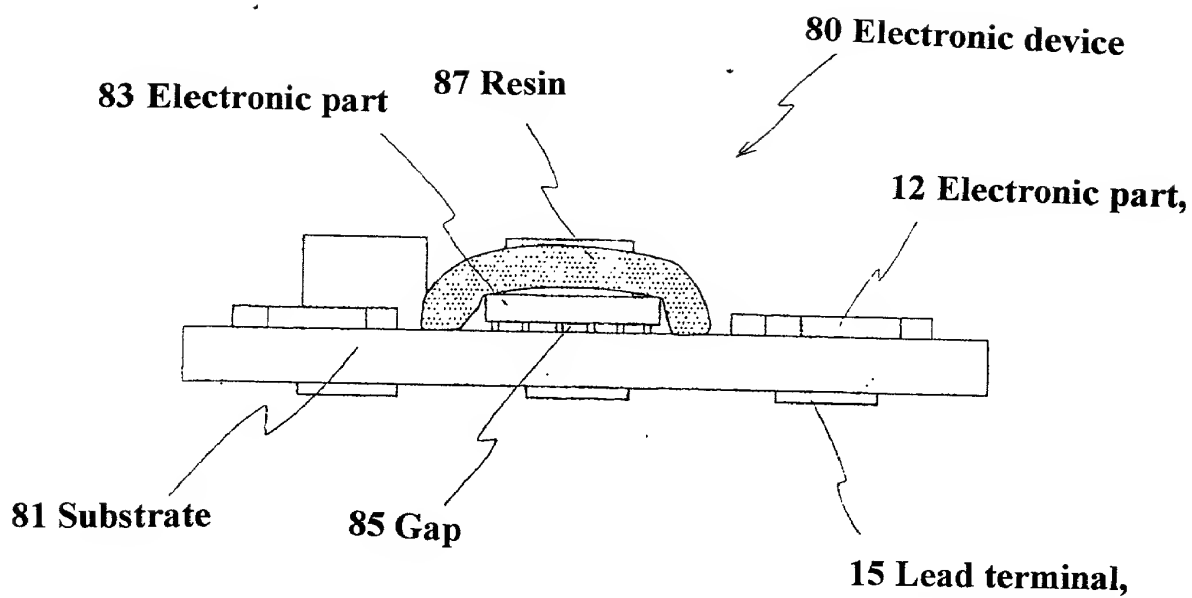
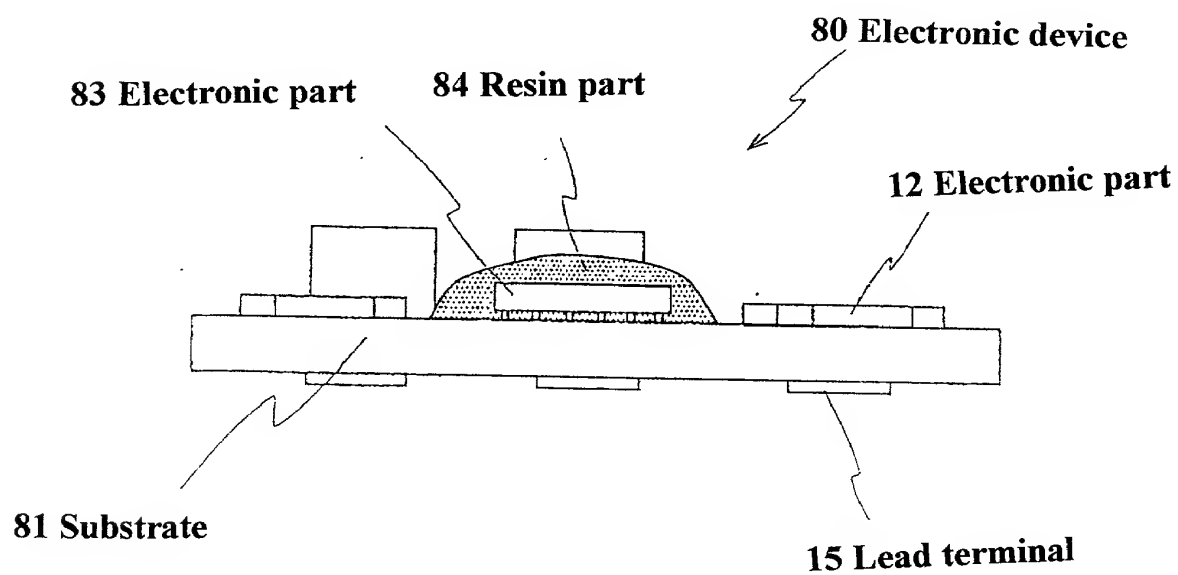


Figure 20



PTO/SB/106 (5-00)

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Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

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## Declaration and Power of Attorney for Patent Application

## 特許出願宣言書及び委任状

## Japanese Language Declaration

## 日本語宣言書

私は、以下に記名された発明者として、ここに下記のとおり宣言する：

As a below named inventor, I hereby declare that:

私の住所、郵便の宛先そして国籍は、私の氏名の後に記載された通りである。

My residence, post office address and citizenship are as stated next to my name

下記の名称の発明について、特許請求範囲に記載され、且つ特許が求められている発明主題に関して、私は、最初、最先且つ唯一の発明者である（唯一の氏名が記載されている場合）か、或いは最初、最先且つ共同発明者である（複数の氏名が記載されている場合）と信じている。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

Electronic Device Manufacturing  
MethodElectronic Device and Resin Filling  
Method

上記発明の説明書はここに添付されているが、下記の欄がチェックされている場合は、この限りでない：

the specification of which is attached hereto unless the following box is checked:

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この出願の米国出願番号またはPCT国際出願番号は、  
\_\_\_\_\_ であり、且つ  
\_\_\_\_\_ の日に修正された出願（該当する場合）☐ was filed on \_\_\_\_\_  
as United States Application Number or  
PCT International Application Number  
\_\_\_\_\_ and was amended on  
\_\_\_\_\_ (if applicable)私は、上記の修正書によって修正された、特許請求範囲を含む上記  
説明書を検討し、且つ内容を理解していることをここに表明する。I hereby state that I have reviewed and understand the contents of  
the above identified specification, including the claims, as amended  
by any amendment referred to above.私は、発明規則法第37編規則1.56に定められている、特許  
性について重要な情報を開示する義務があることを認める。I acknowledge the duty to disclose information which is material to  
patentability as defined in Title 37, Code of Federal Regulations,  
Section 1.56

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I hereby claim foreign priority under Title 35, United States Code, Section 119(a)(1) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application for which priority is claimed.

Prior Foreign Application(s)

外国での先行出願

Priority Not Claimed

優先権主張なし

11-198987

(Number)  
(番号)

Japan

(Country)  
(国名)

13/7/1999

(Day/Month/Year Filed)  
(出願日/月/年)☐(Number)  
(番号)(Country)  
(国名)(Day/Month/Year Filed)  
(出願日/月/年)☐

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I hereby claim the benefit under Title 35, United States Code, Section 119(a) of any United States provisional application(s) listed below.

(Application No.)  
(出願番号)(Filing Date)  
(出願日)(Application No.)  
(出願番号)(Filing Date)  
(出願日)

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(Application No.)  
(出願番号)(Filing Date)  
(出願日)(Status: Patented, Pending, Abandoned)  
(状況: 特許許可、係属中、放棄)(Application No.)  
(出願番号)(Filing Date)  
(出願日)(Status: Patented, Pending, Abandoned)  
(状況: 特許許可、係属中、放棄)

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POWER OF ATTORNEY. As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number).

送達先

Send Correspondence to:

Norman H. Beamer, Esq. (Reg. No. 32,721)  
Nicola A. Pisano, Esq. (Reg. No. 34,408)  
Fish & Neave  
1251 Avenue of the Americas, New York, New York, 10020

直接電話連絡先。(氏名及び電話番号)

Direct Telephone Calls to (name and telephone number)

Norman H. Beamer, Esq.  
650.617.4030

第一または第一発明者氏名

Full name of sole or first inventor

Gosuke Oshima

発明者の署名

日付

Inventor's signature

Date

G. Oshima July 12, 2000

住所

Residence

4357-4 Tsunobuchi, Tamamura-machi, Sawa-gun, Gunma, Japan

国籍

Citizenship

Japanese

郵便の宛先

Post Office Address

56072 Nakamuroda, Haruna-machi

Gunma-gun, Gunma, 370-3347, Japan

第二共同発明者がある場合、その氏名

Full name of second joint inventor, if any

Masashi Miki

第二共同発明者の署名

日付

Second inventor's signature

Date

M. Miki July 12, 2000

住所

Residence

1-46-1-305 Dote-cho, Omiya-shi, Saitama, 330-0801, Japan

国籍

Citizenship

Japanese

郵便の宛先

Post Office Address

5607-2 Nakamuroda, Haruna-machi

Gunma-gun, Gunma, 370-3347, Japan

(第三以下の共同発明者についても同様に記載し、署名をすること)

(Supply similar information and signature for third and subsequent joint inventors.)